**OBDH Documentation Report**

**(22-1-15)**

**Name of the Member: Saransh Gupta**

Task Done: CoDR Studied

**Topics in brief:**

* Hardware components studied. Microcontroller & Types of Architecture studied.
* Components of the Subsystem. Processors: Architecture and classification studied.
* Memory and choice of memory analyzed.
* Bus topology and detailed description given.
* Micro controller and Software studied.

**Outcome:**

The material gives great inside the basic concepts to be used in the subsystems. The description and algorithms provided gives us full detail for the working of the bus. Also, the work done till now, and the various reasons for the selection of components have been listed.

But, there has been a little problem in understanding some new terminologies.